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In re the application of:

HENRY CHUNG

Docket: 30-4718 (4780)

Serial Number: 09/328,645

Group Art Unit: 2811

Filed: June 9, 1999

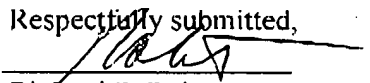
Examiner: H. Vu

For: A FABRICATION METHOD OF INTEGRATED CIRCUITS WITH
BORDERLESS VIAS AND LOW DIELECTRIC-CONSTANT INTER-METAL
DIELECTRICSCERTIFICATIONAssistant Commissioner for Patents
Washington, D.C. 20231
ATTN: MS. LISSI MARQUIS

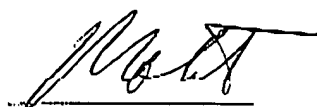
Sir:

In connection with the request to reinstate the above application, I hereby certify on my own personal knowledge, that the Response to the Restriction Requirement mailed December 23, 1999 was facsimile transmitted to the Patent and Trademark Office on January 14, 2000.

Respectfully submitted,


Richard S. Roberts
Reg. No. 27,941
P.O. Box 484
Princeton, New Jersey 08542
(609) 921-3500
Date: January 22, 2002

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office (FAX No. 703-305-1034) on January 22, 2002.


Richard S. Roberts
Reg. No. 27,941

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BORDERLESS VIAS AND LOW DIELECTRIC-CONSTANT INTER-METAL
DIELECTRICS****FAX COVER SHEET****TO:** Assistant Commissioner for Patents
Washington, D.C. 20231**FAX NO.:** (703) 305-1034**FROM:** Richard S. Roberts
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(609) 921-3500**KINDLY DIRECT THIS COMMUNICATION TO:****EXAMINER :** Lissi Marquis
GROUP : 2800**NO. OF PAGES SENT INCLUDING THIS COVER SHEET: 2**

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